

FIG. 1

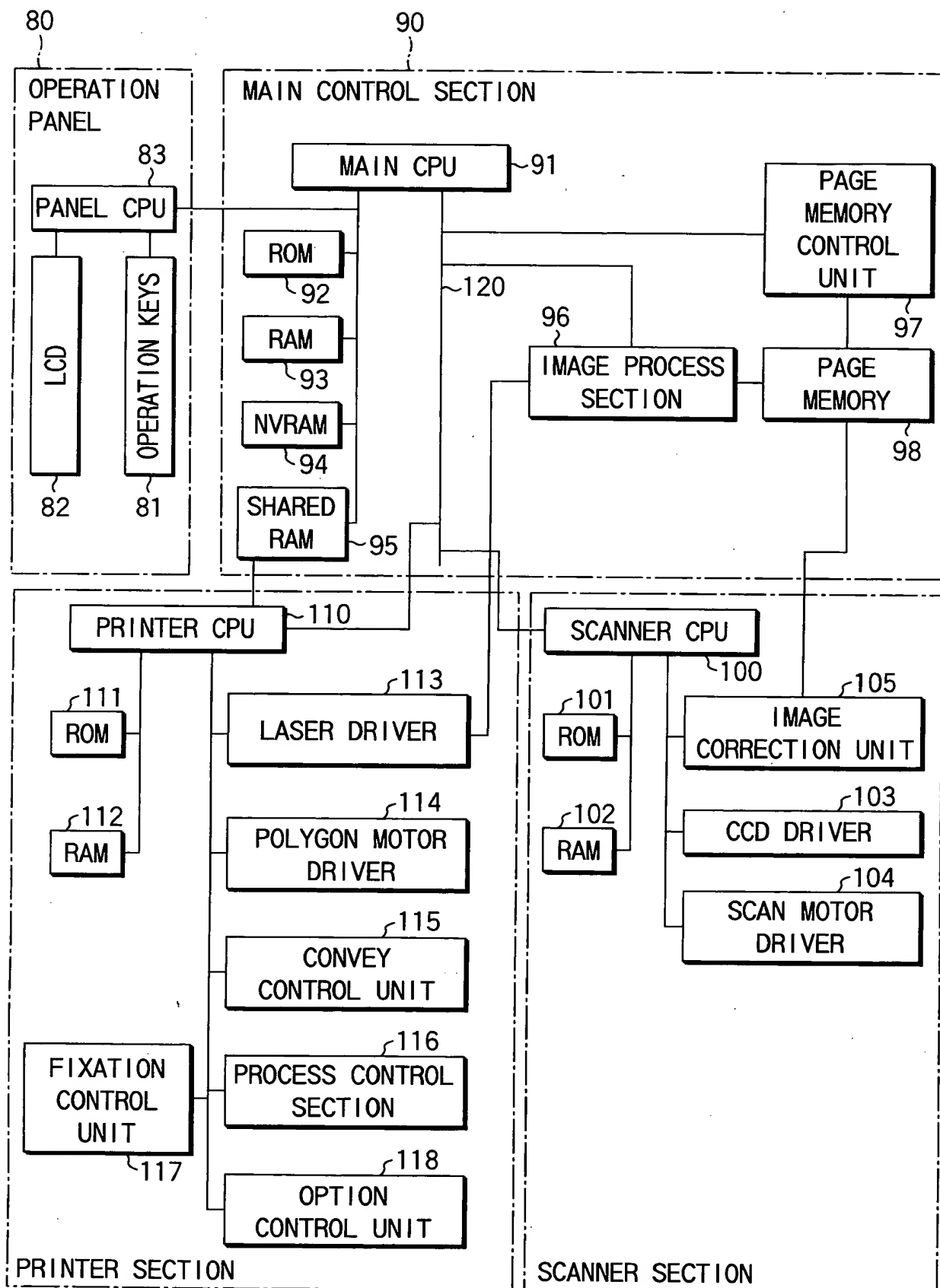


FIG. 2

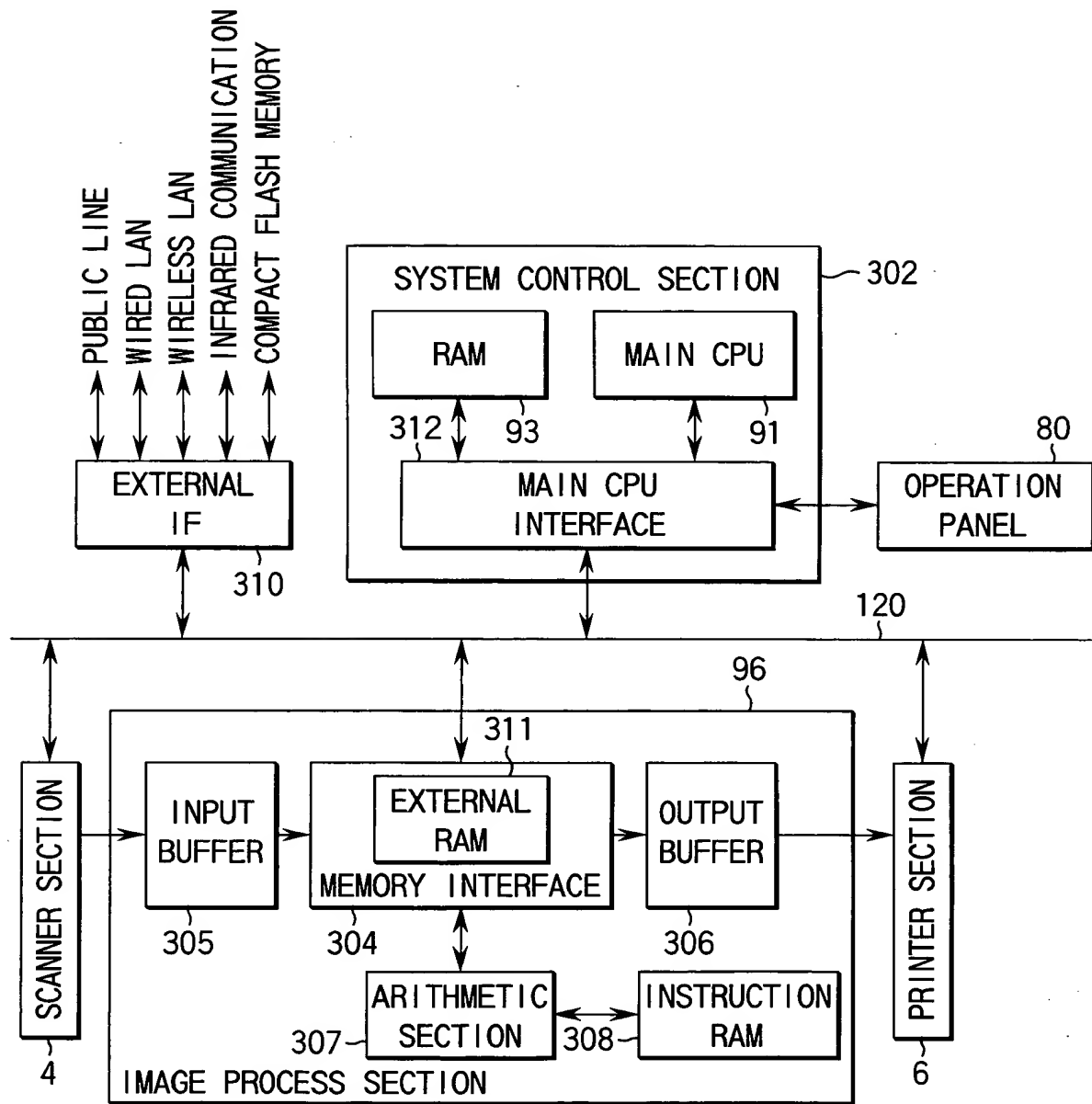


FIG.3

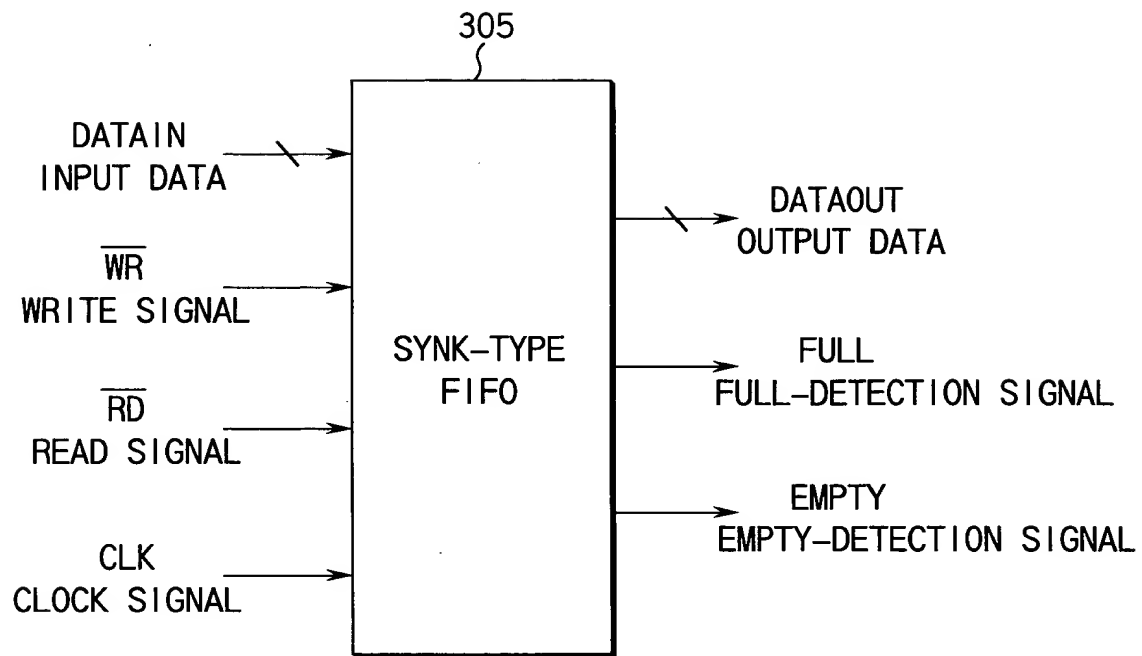


FIG.5

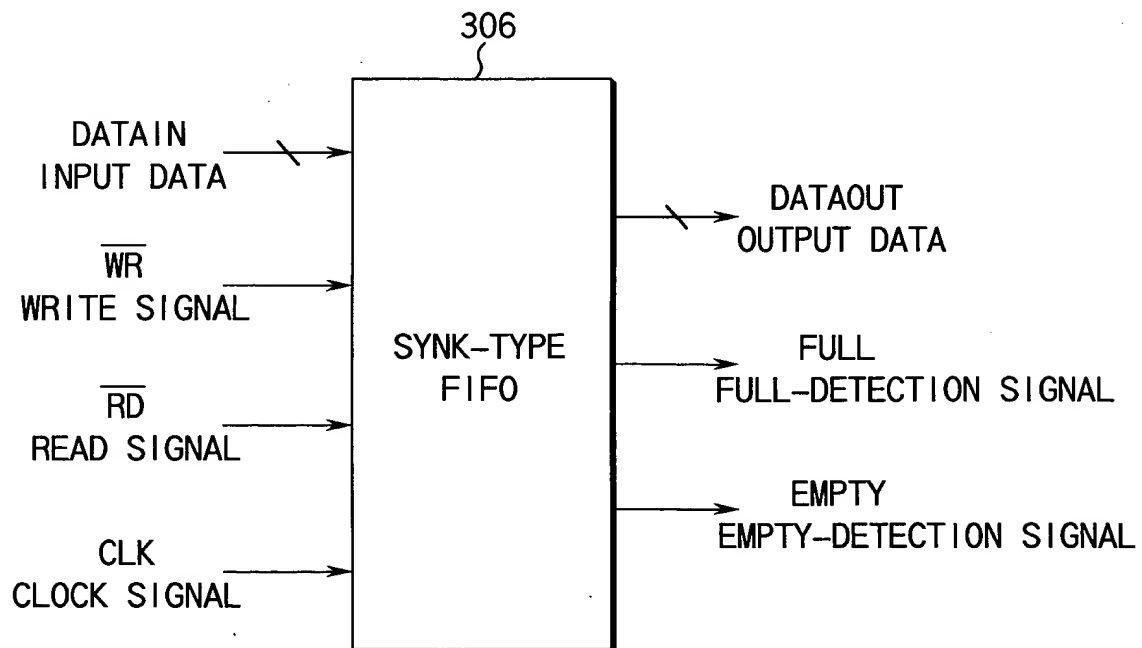


FIG.6

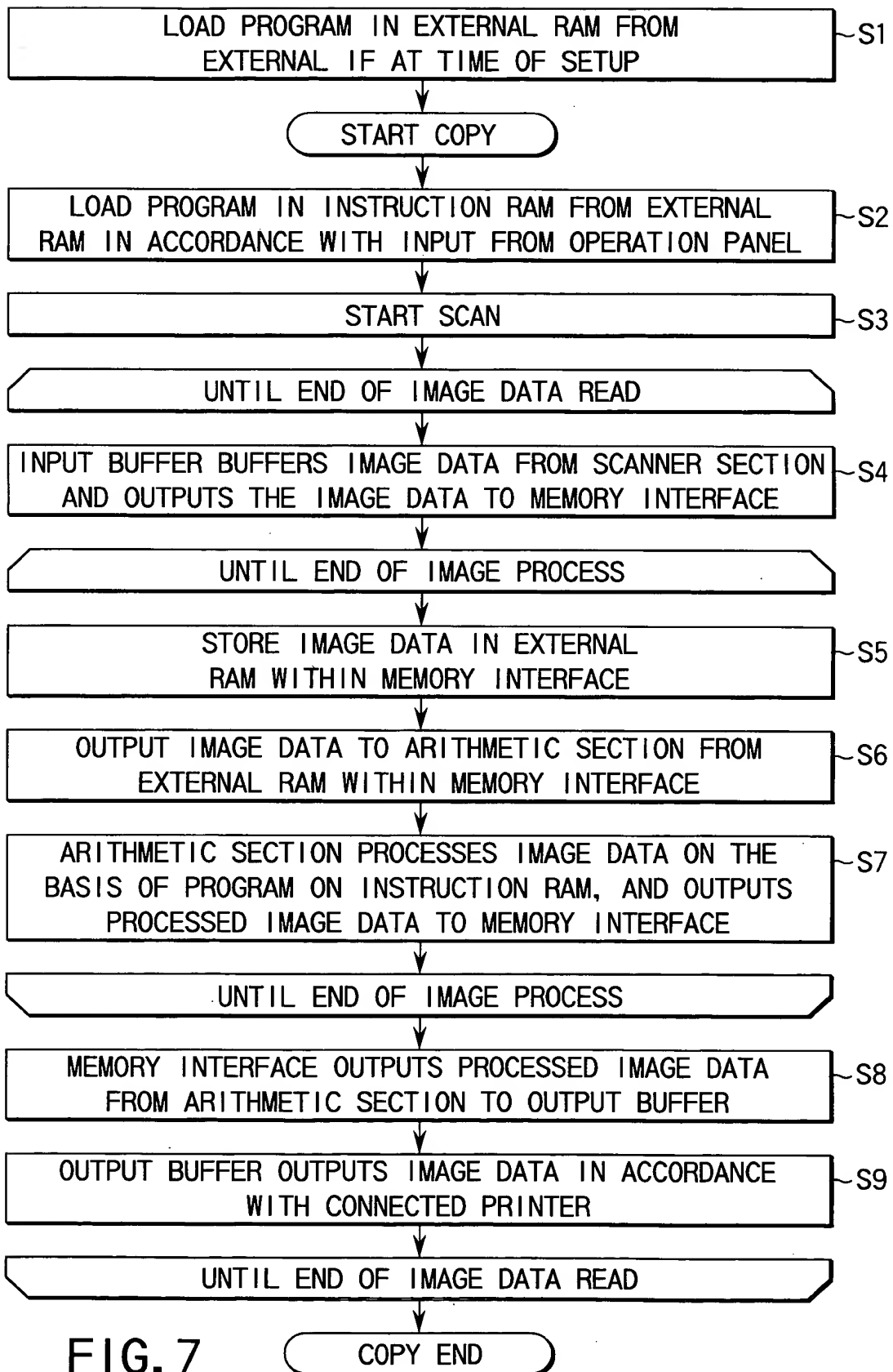


FIG. 7

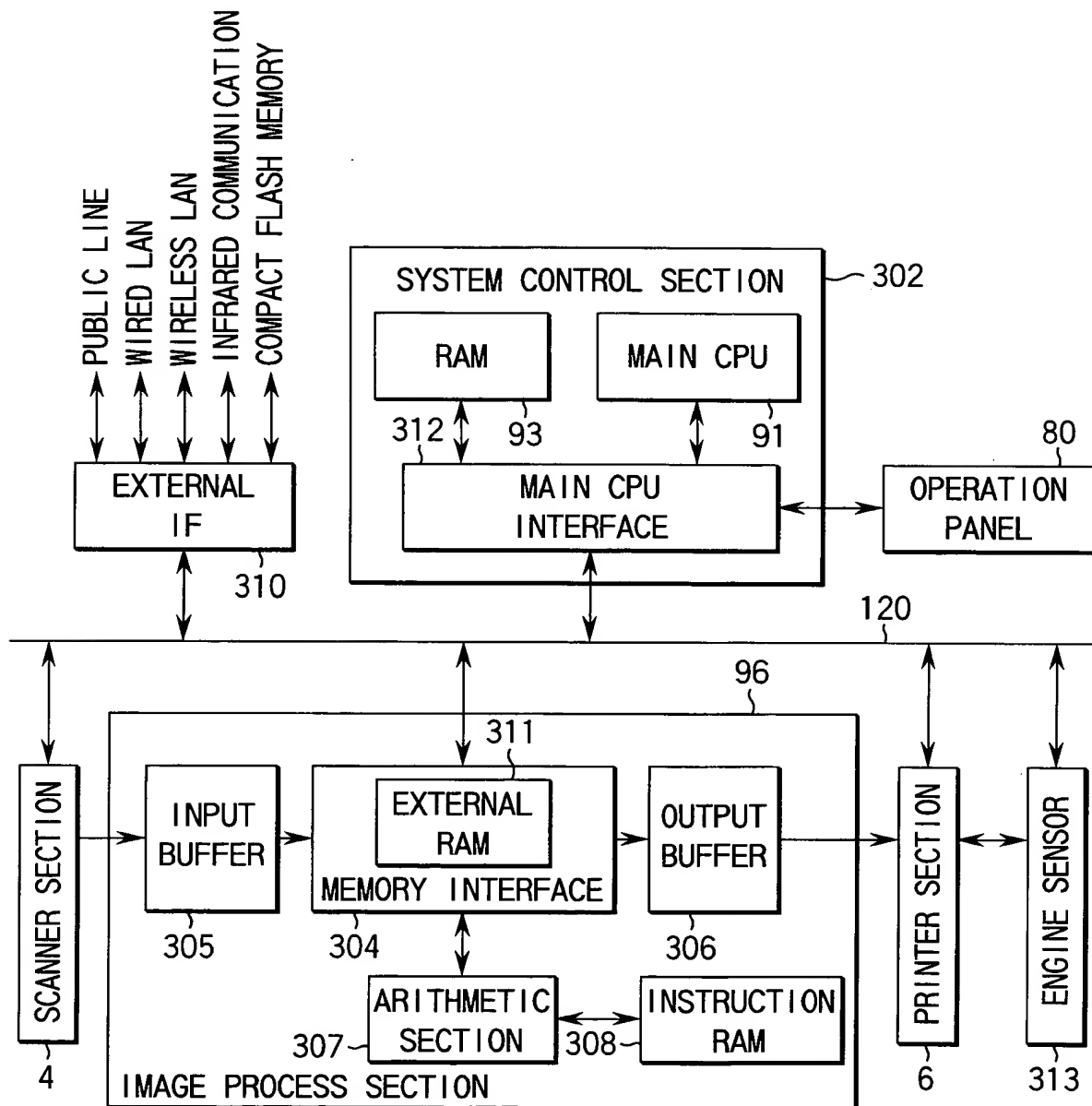
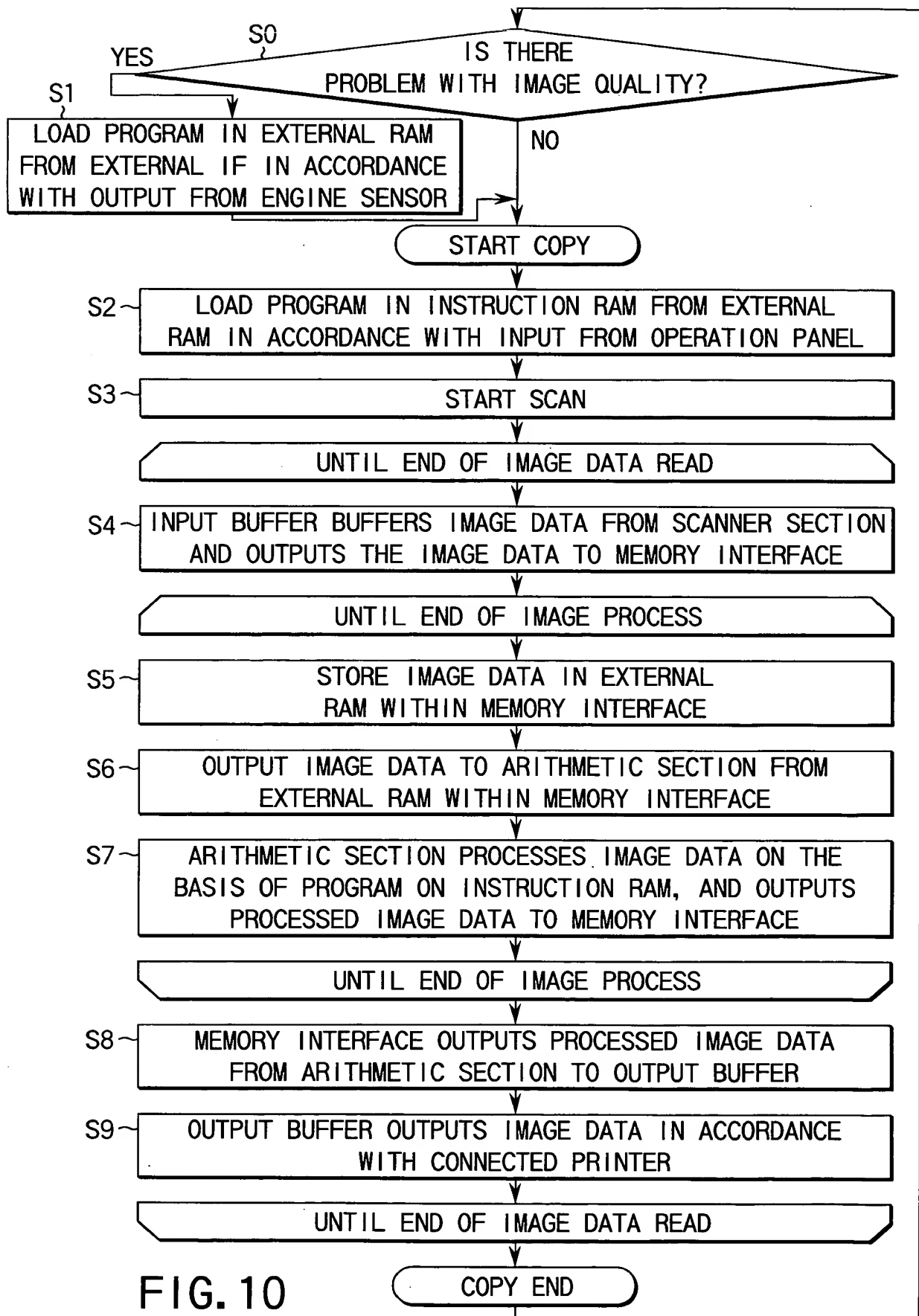


FIG.8



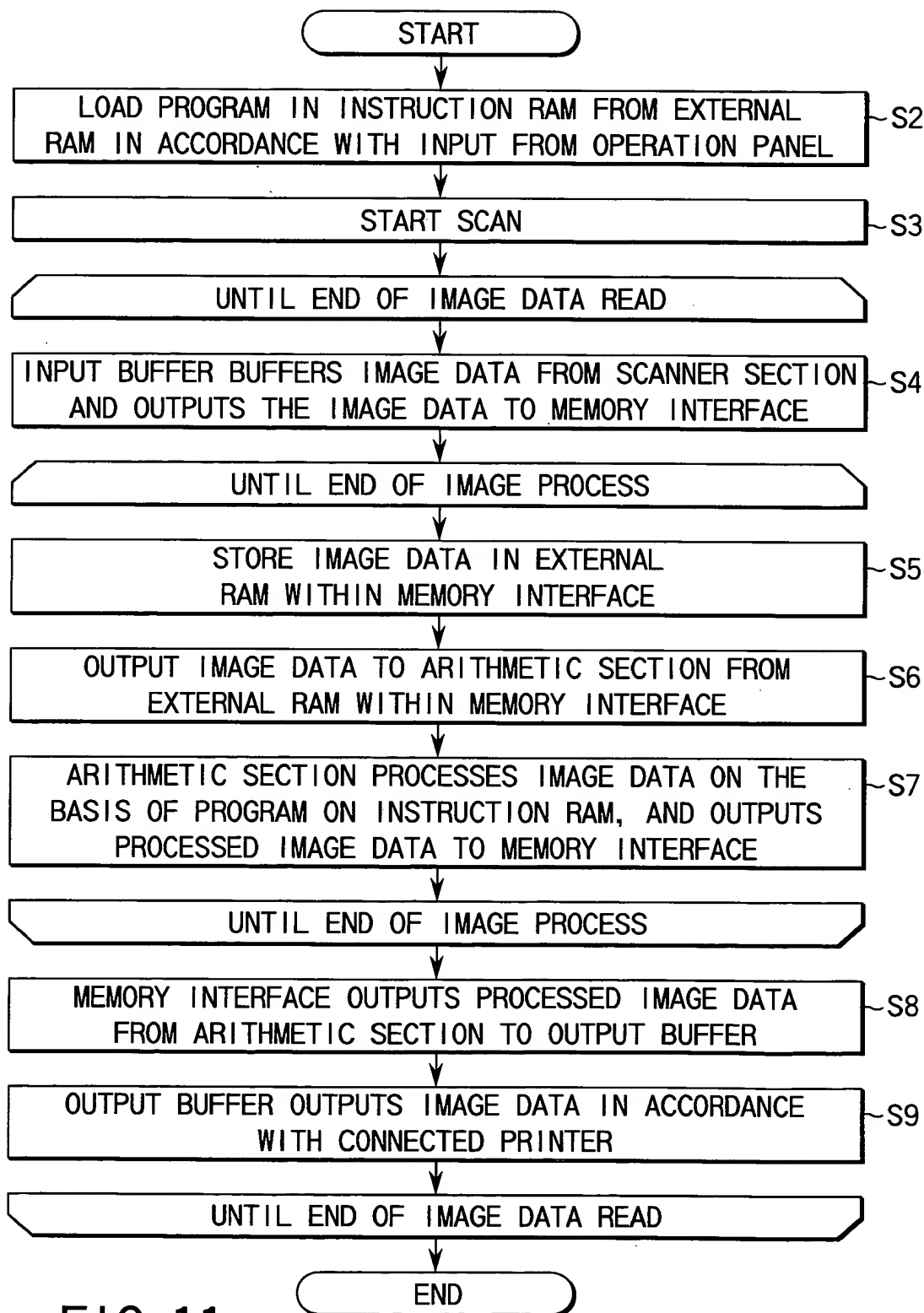


FIG. 11

0000036300

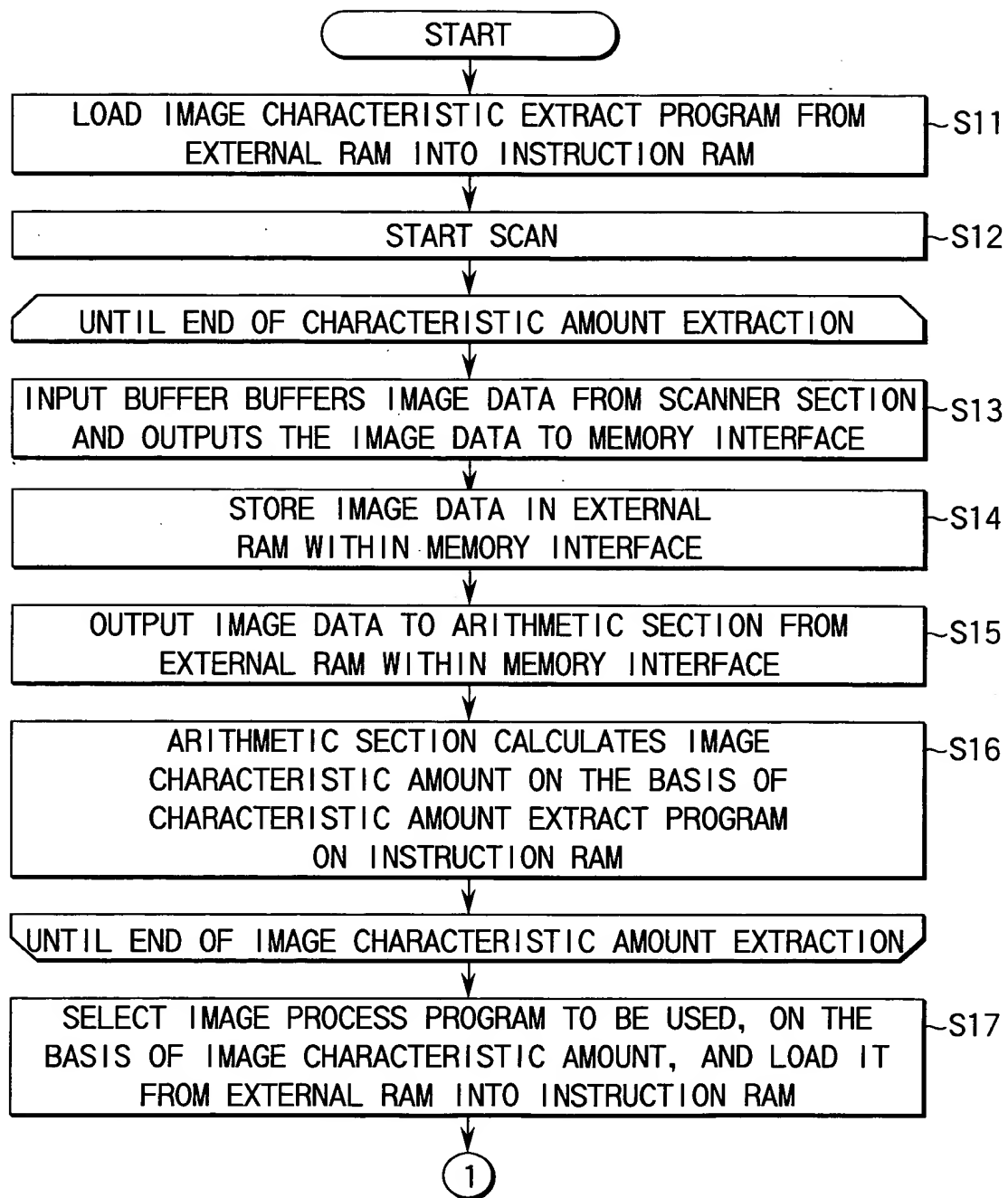


FIG. 12

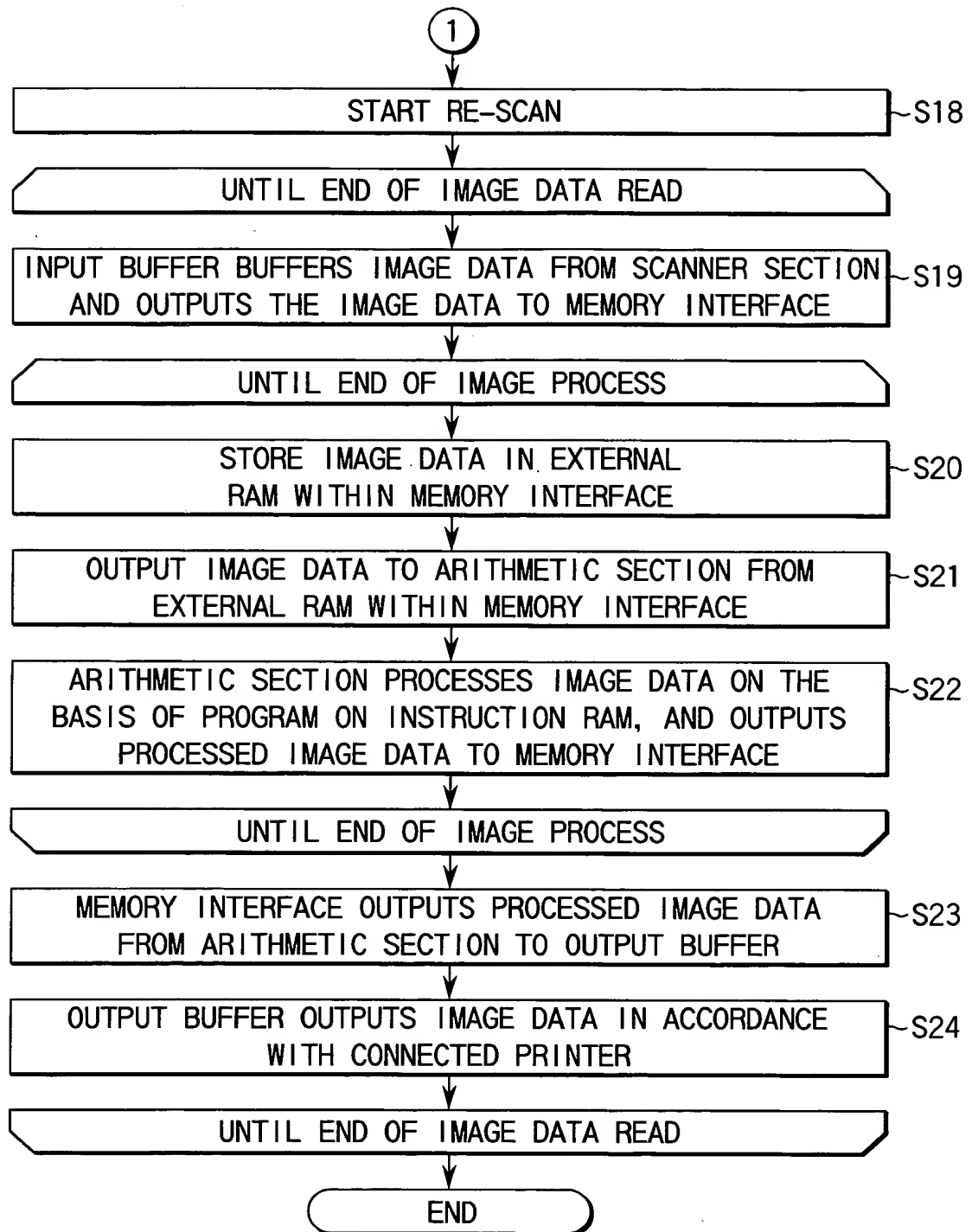


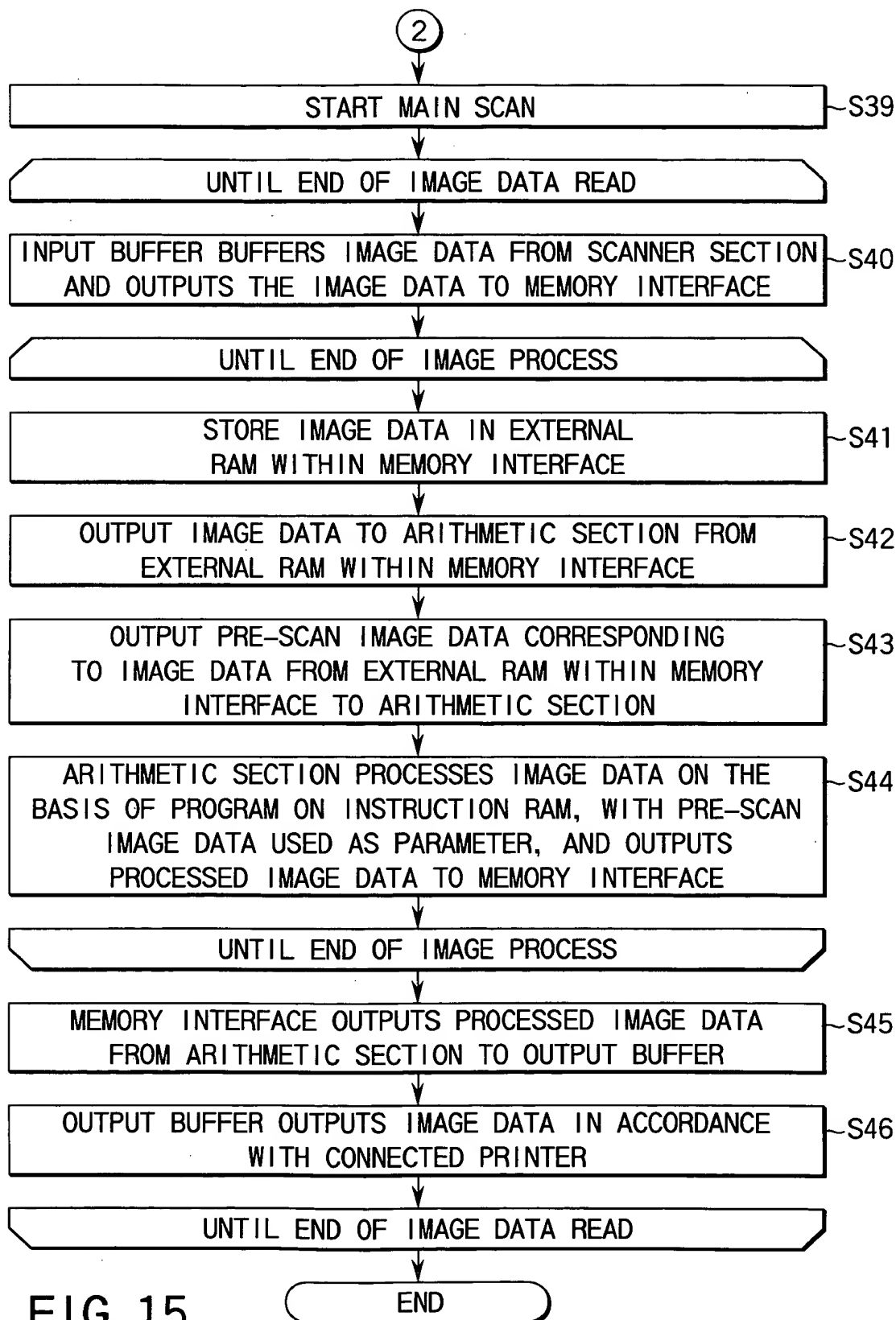
FIG. 13

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graph TD
    START([START]) --> S31[LOAD PRE-SCAN IMAGE PROCESS PROGRAM FROM  
EXTERNAL RAM INTO INSTRUCTION RAM]
    S31 --> S32[START PRE-SCAN]
    S32 --> S33[/UNTIL END OF PRE-SCAN/]
    S33 --> S34[INPUT BUFFER BUFFERS IMAGE DATA FROM SCANNER SECTION  
AND OUTPUTS THE IMAGE DATA TO MEMORY INTERFACE]
    S34 --> S35[STORE IMAGE DATA IN EXTERNAL  
RAM WITHIN MEMORY INTERFACE]
    S35 --> S36[OUTPUT IMAGE DATA TO ARITHMETIC SECTION FROM  
EXTERNAL RAM WITHIN MEMORY INTERFACE]
    S36 --> S37[ARITHMETIC SECTION CALCULATES PRE-SCAN  
IMAGE DATA ON THE BASIS OF PRE-SCAN  
PROGRAM ON INSTRUCTION RAM]
    S37 --> S38[STORE PRE-SCAN IMAGE DATA IN EXTERNAL  
RAM WITHIN MEMORY INTERFACE]
    S38 --> S39[/UNTIL END OF PRE-SCAN/]
    S39 --> S40[LOAD MAIN SCAN PROGRAM FROM EXTERNAL RAM  
INTO INSTRUCTION RAM]
    S40 --> 2((2))

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FIG. 14



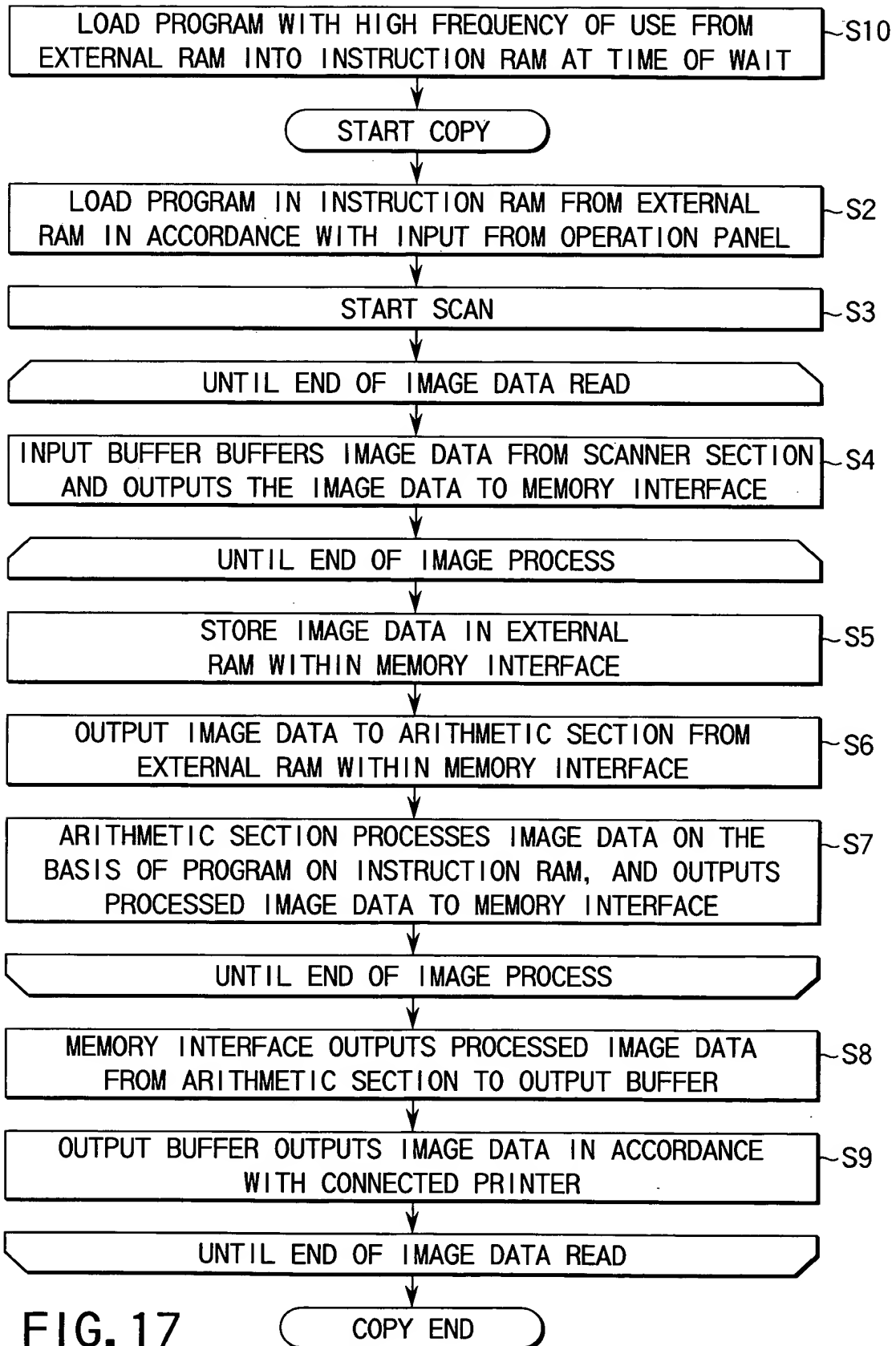


FIG. 17

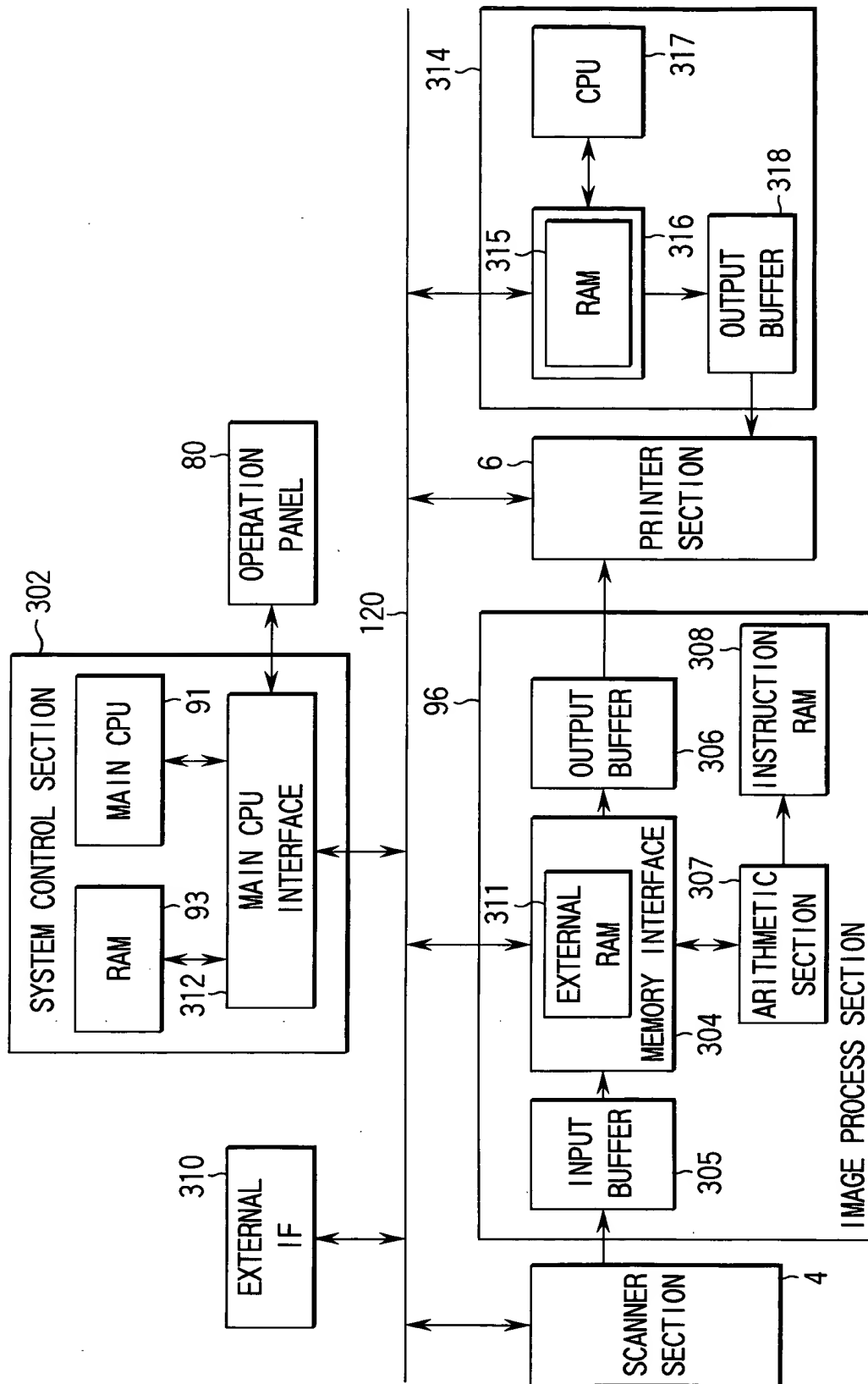


FIG.18

—◆— 1-PIXEL MODULATION
—■— 2-PIXEL MODULATION

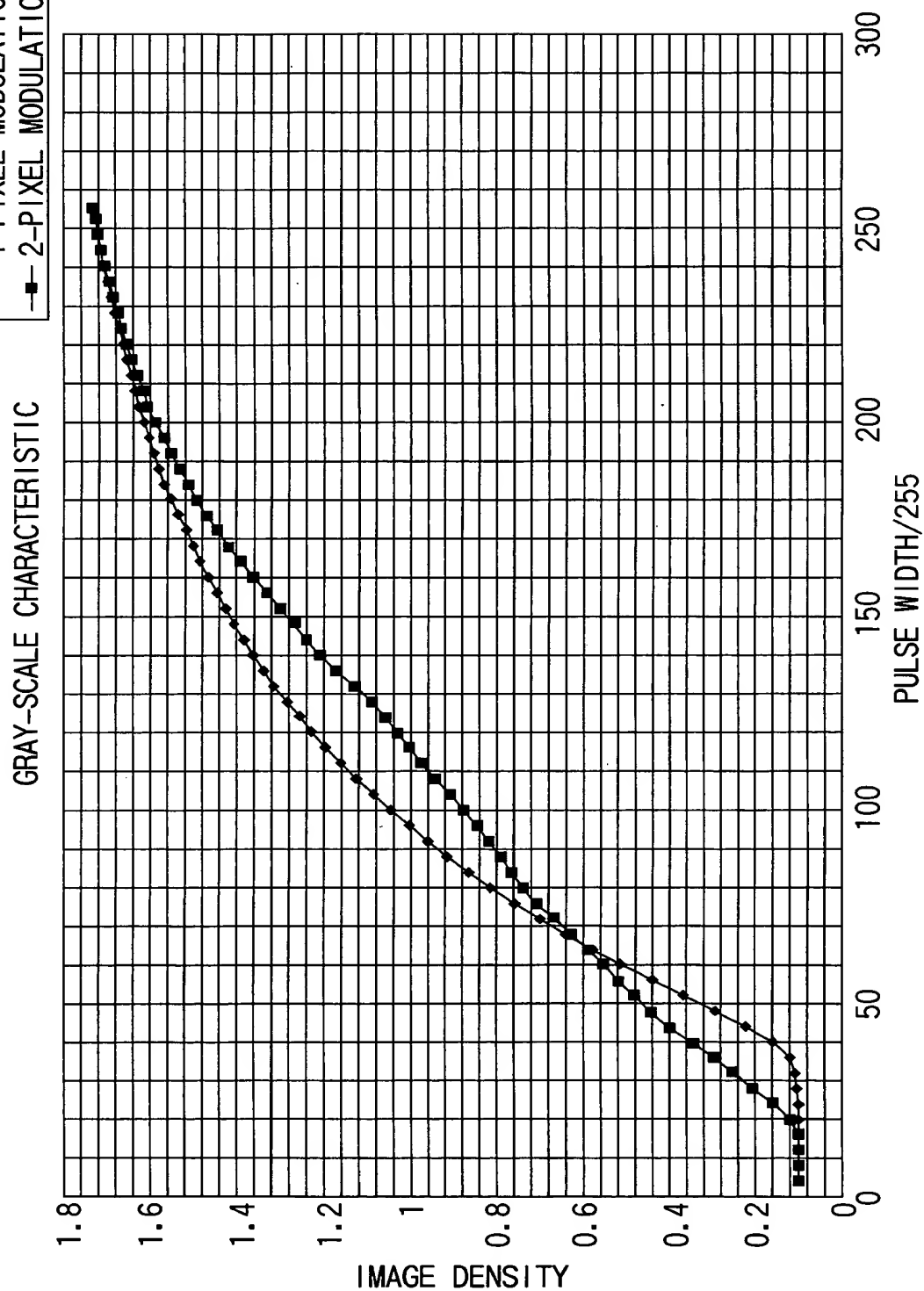


FIG. 19